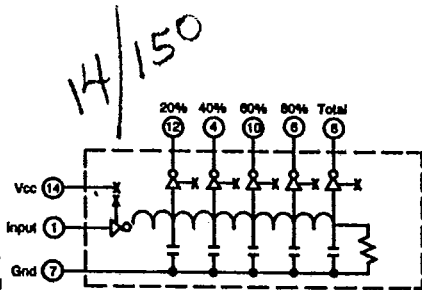
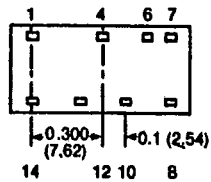
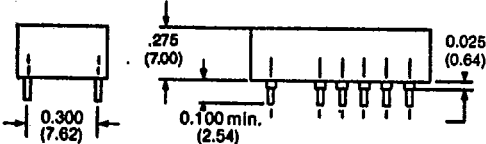
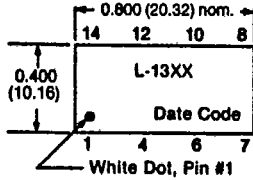
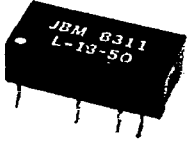


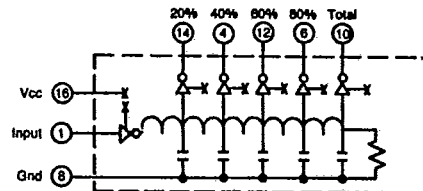
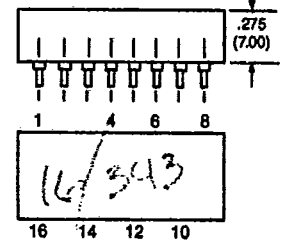
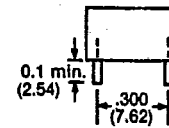
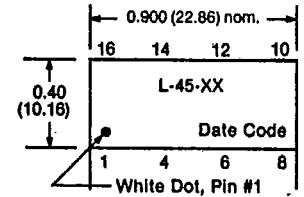
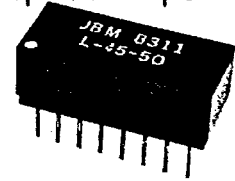
Active Delay Lines

Delay Range 25 thru 1000 nsec
STYLE L-13 (14-pin outline)
 5 equally-spaced taps



Dimensions in Inches (mm).

T-47-13
Delay Range 25 thru 1000 nsec
STYLE L-45 (16-pins)
 5 equally-spaced taps



Dimensions in Inches (mm).

INPUT CONDITIONS (See Page 3)

Operating Temp.....0° to +70°C
 Storage Temp..... -55°C to +125°C
 (All lines internally terminated)

I_{IH} Logic "1" Input Current.....50µA Max.
 I_{IL} Logic "0" Input Current..... -2mA Max.
 V_{OH} Logic "1" Voltage Out.....2.7V Min.
 V_{OL} Logic "0" Voltage Out.....0.5V Max.

DRIVE CAPABILITIES (TTL LOADS)

Logic "0" { 10 Loads/Tap Max.
 20 Loads/Unit Max.
 Logic "1" 20 Loads/Unit Max.

Supply Current 75mA Typ.
 Voltage Range 4.75V to 5.25V

TOTAL DELAY (nsec ± 5%)	TAP INTERVALS (nsec*)	RISE TIME (ns MAX) (0.8 to 2.0V)	JBM PART NUMBER L-	
			L-13 (14) PIN	L-45 (16) PIN
25	5 ± 3ns	2.0	L-13-50 ✓	L-45-50 ✓
30	6 ± 3ns	2.0	L-13-51 ✓	L-45-51 ✓
35	7 ± 3ns	2.0	L-13-52 ✓	L-45-52 ✓
40	8 ± 3ns	2.0	L-13-53 ✓	L-45-53 ✓
45	9 ± 3ns	2.0	L-13-54 ✓	L-45-54 ✓
50	10 ± 3ns	2.0	L-13-55 ✓	L-45-55 ✓
75	15 ± 3ns	2.0	L-13-56 ✓	L-45-56 ✓
100	20 ± 3ns	2.0	L-13-57 ✓	L-45-57 ✓
150	30 ± 3ns	2.5	L-13-58 ✓	L-45-58 ✓
200	40 ± 3ns	3.0	L-13-59 ✓	L-45-59 ✓
250	50 ± 3ns	3.0	L-13-60 ✓	L-45-60 ✓
300	60 ± 5%	3.5	L-13-250 ✓	L-45-250 ✓
350	70 ± 5%	3.5	L-13-251 ✓	L-45-251 ✓
400	80 ± 5%	4.0	L-13-252 ✓	L-45-252 ✓
450	90 ± 5%	4.0	L-13-253 ✓	L-45-253 ✓
500	100 ± 5%	4.0	L-13-254 ✓	L-45-254 ✓
600	120 ± 5%	4.5	L-13-255 ✓	L-45-255 ✓
700	140 ± 5%	4.5	L-13-256 ✓	L-45-256 ✓
750	150 ± 5%	4.5	L-13-257 ✓	L-45-257 ✓
800	160 ± 5%	5.0	L-13-258 ✓	L-45-258 ✓
900	180 ± 5%	5.0	L-13-259 ✓	L-45-259 ✓
1000	200 ± 5%	5.0	L-13-260 ✓	L-45-260 ✓

*25ns - 250ns TAP/TAP; 300ns - 1,000ns cumulative @ 25°C, 5.00VDC.

Delays may change 2% or 1ns for a respective 5% increase or decrease in supply voltage.

Consult factory for other delays, tolerances, and logic families.